

PRIOR ART  
FIG.1

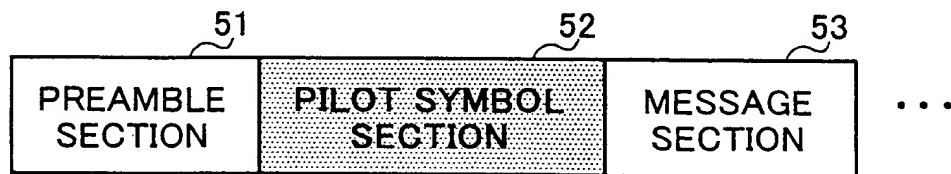
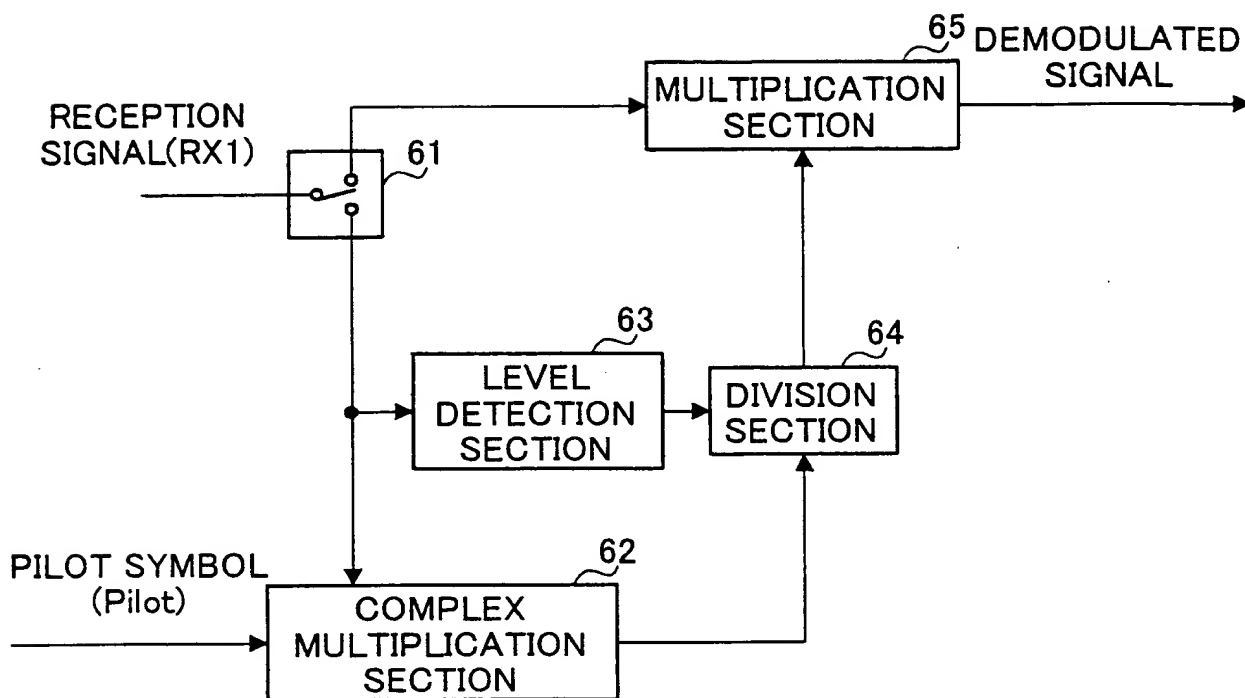
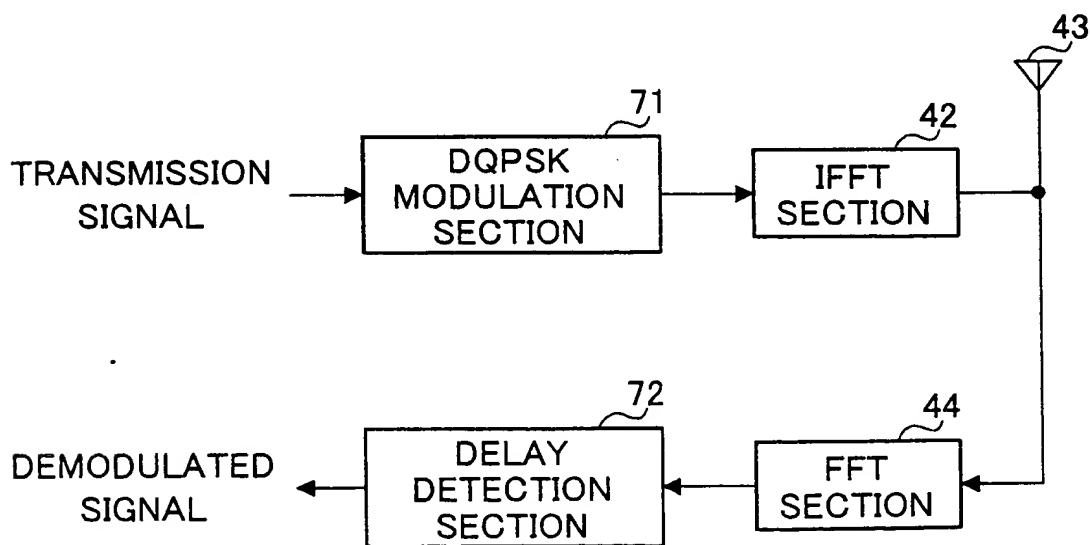


FIG.2



PRIOR ART  
FIG.3



PRIOR ART  
FIG.4

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graph LR
    RX1[RECEPTION SIGNAL(RX1)] --> J(( ))
    J --> MS[MULTIPLICATION SECTION 82]
    J --> DS[DELAY SECTION 81]
    DS --> MS
    MS --> DSIGNAL[DEMODULATED SIGNAL]
  
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PRIOR ART

FIG.5

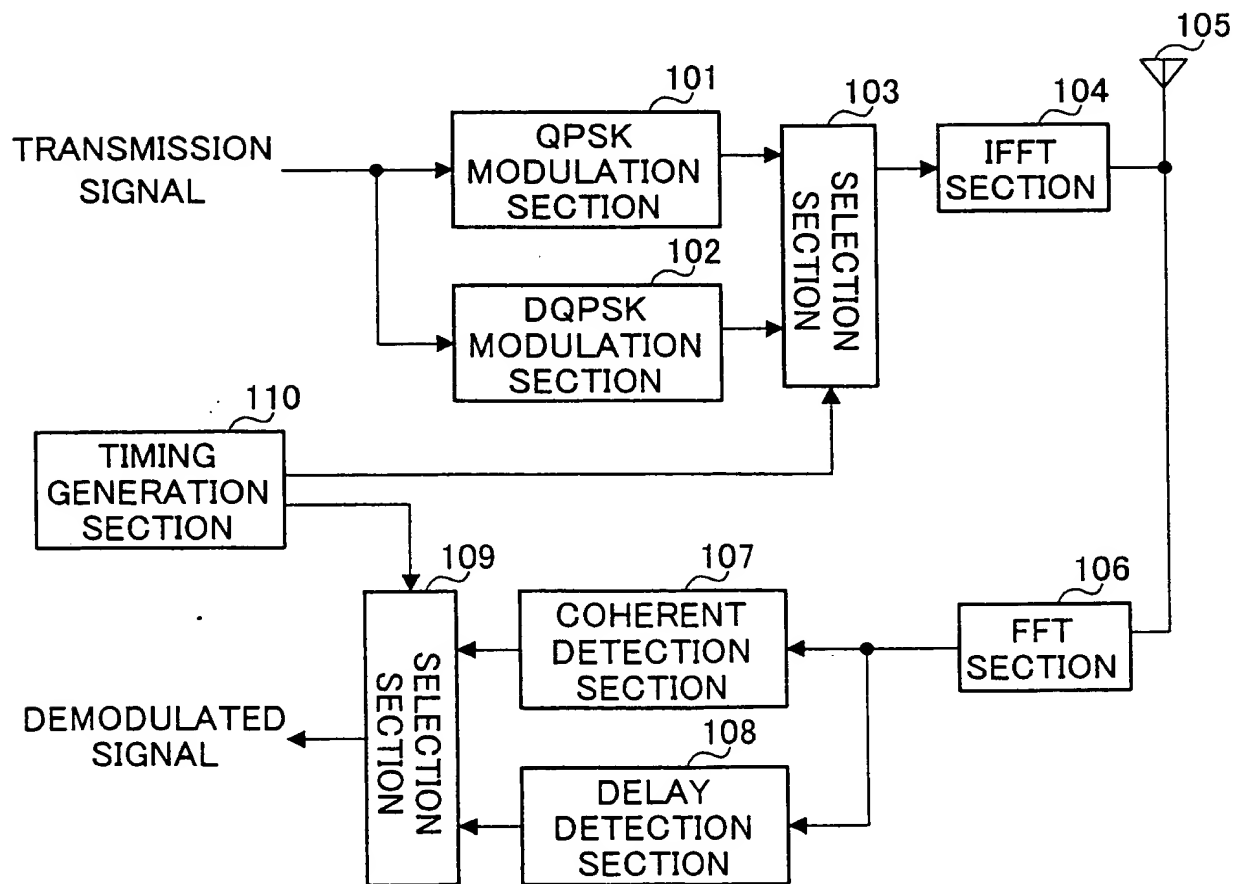


FIG.6

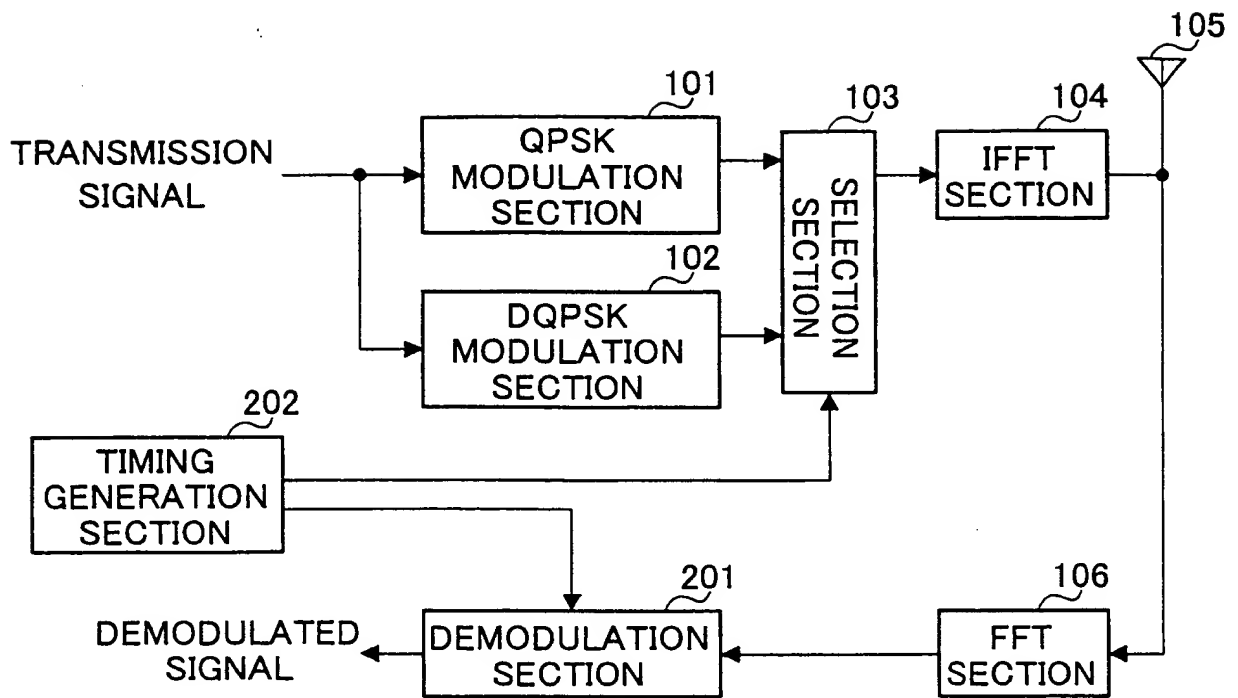


FIG.7

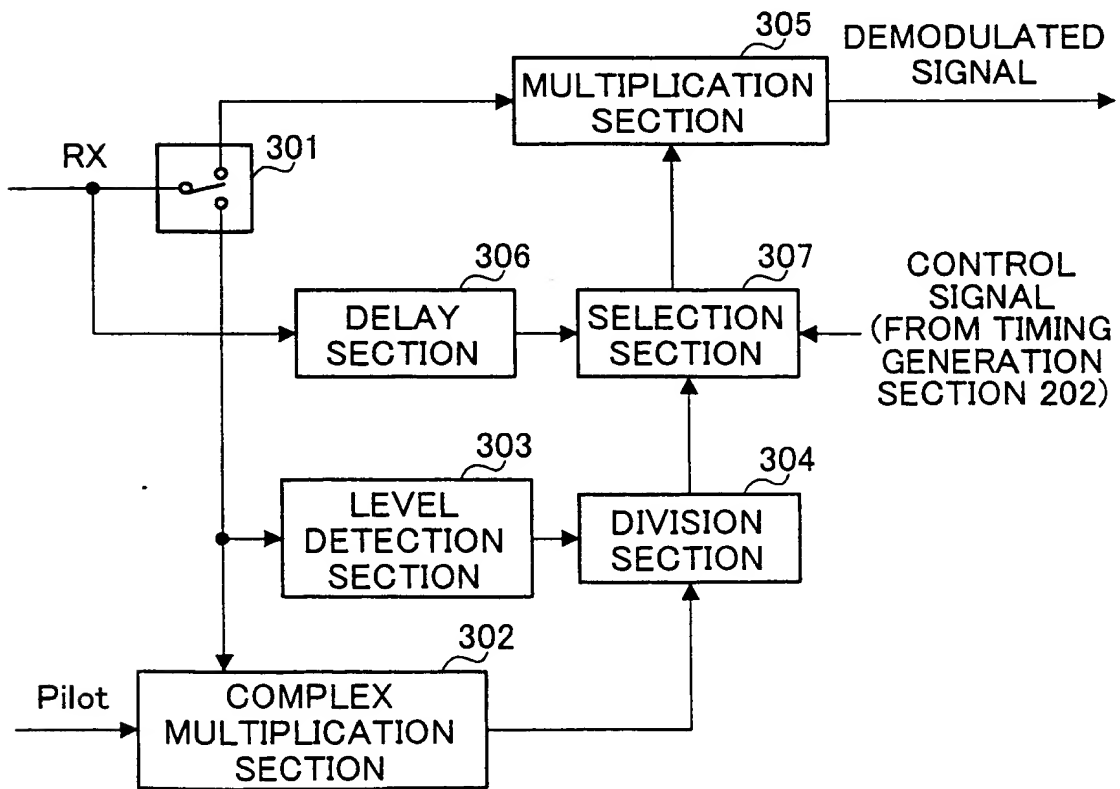


FIG.8